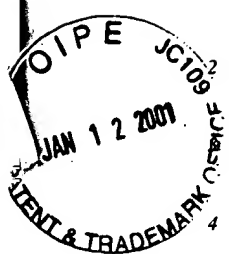


#3



DECLARATION OF JOINT INVENTORS FOR PATENT APPLICATION

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: Capacitor Fabrication Methods and Capacitor Constructions, Serial No. 09/653,156, filed August 31, 2000.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims.

I acknowledge the duty to disclose information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations §1.56.

PRIOR FOREIGN APPLICATIONS:

I hereby state that no applications for foreign patents or inventor's certificates have been filed prior to the date of execution of this declaration.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statement

1 may jeopardize the validity of the application or any patent issued
2 therefrom.

3 * * * * *

4 Full name of inventor: Vishnu K. Agarwal

5 Inventor's Signature: _____

6 Date: 11/28/00

7 Residence: Boise, Idaho

8 Citizenship: India

9 Post Office Address: 2463 E. Red Cedar Lane, #103
Boise, ID 83716

10 * * * * *

11 Full name of inventor: Garry A. Mercaldi

12 Inventor's Signature: _____

13 Date: 11/28/00

14 Residence: Boise, Idaho

15 Citizenship: U.S.

16 Post Office Address: 2455 S. Pine Bar Place
17 Meridian, ID 83642
18
19
20
21
22



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/653,156
Filing Date August 31, 2000
Inventor Vishnu K. Agarwal, et al.
Assignee Micron Technology, Inc.
Group Art Unit Unassigned
Examiner Unassigned
Attorney's Docket No. MI22-1518
Title: Capacitor Fabrication Methods and Capacitor Constructions

**POWER OF ATTORNEY BY ASSIGNEE AND
CERTIFICATE BY ASSIGNEE UNDER 37 CFR §3.73(b)**

To: Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

MICRON TECHNOLOGY, INC., the Assignee of the entire right, title and interest in the above-identified patent application by assignment attached hereto, hereby appoints the attorneys and agents of the firm of **WELLS, ST. JOHN, ROBERTS, GREGORY & MATKIN P.S.**, listed as follows:

David P. Roberts	Reg. No. 23,032
Randy A. Gregory	Reg. No. 30,386
Mark S. Matkin	Reg. No. 32,268
James L. Price	Reg. No. 27,376
Deepak Malhotra	Reg. No. 33,560
Mark W. Hendricksen	Reg. No. 32,356
David G. Latwesen	Reg. No. 38,533
George G. Grigel	Reg. No. 31,166
Keith D. Grzelak	Reg. No. 37,144
James D. Shaurette	Reg. No. 39,833
Frederick M. Fliegel	Reg. No. 36,138
Donald Brent Kenady	Reg. No. 40,045
James E. Lake	Reg. No. 44,854
Bernard Berman	Reg. No. 37,279

1 and also attorneys Michael L. Lynch (Reg. No. 30,871) and Lia Pappas
2 Dennison (Reg. No. 34,095) of Micron Technology, Inc., as its attorneys
3 with full power of substitution to prosecute this application and transact all
4 business in the Patent and Trademark Office connected therewith.

5 The Assignee certifies that the above-identified Assignment has been
6 reviewed and to the best of Assignee's knowledge and belief, title is in the
7 Assignee, and a copy of the Assignment is submitted herewith.


8 Please direct all correspondence regarding this application to:

9 Customer No. 021567
10 Wells, St. John, Roberts, Gregory & Matkin P.S.
11 Attn: James E. Lake
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Spokane, WA 99201-3828

12 Telephone: (509) 624-4276
13 Facsimile: (509) 838-3424

14 MICRON TECHNOLOGY, INC.

15
16 Dated: 12-2-00

By: 

17 Name: Michael L. Lynch, Esq.
18 Title: Chief Patent Counsel

19 Attachment: Copy of Assignment; Copy of Board of Directors' Resolution
20
21
22
23